

In re Patent Application of:

SWAMI

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In the Specification:

Please replace the paragraph beginning at page 8, line 1, with the following rewritten paragraph:

Two look-up tables **L1** are required for a full adder.

As used herein, a "full adder" refers to both an adder and a subtractor. That is, one is for sum generation and one is for carry generation. With some modifications in the prior art look-up table L1, one look-up table can advantageously be used for implementing two full adders. Accordingly, a modification of the look-up table **L1** in accordance with the present invention is ~~illustrativley~~ shown in FIGS. 2 and 4 which can provide two sum outputs on leads OUT0 and OUT1, with OUT0 as LSB and OUT1 as MSB, and outputs required to generate carry on leads C_L and C_Li corresponding to the LSB, and C_U and C_Ui corresponding to the MSB.